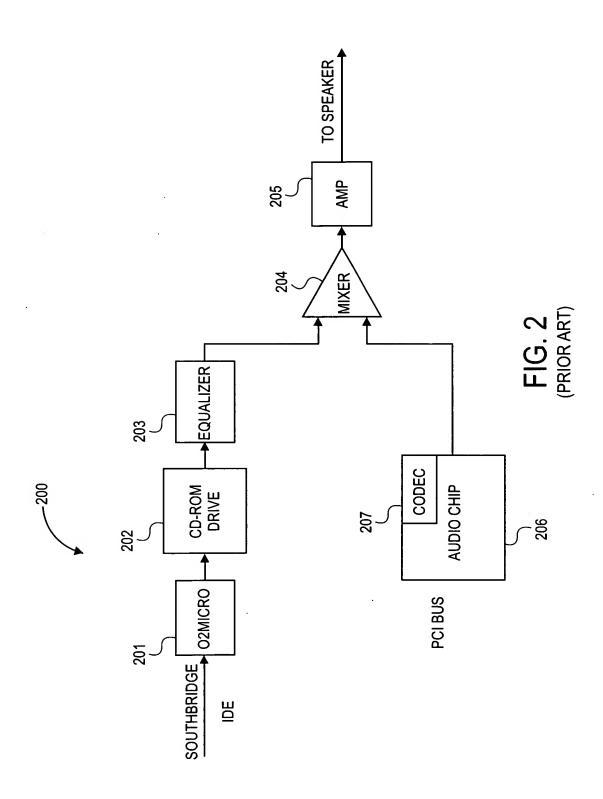
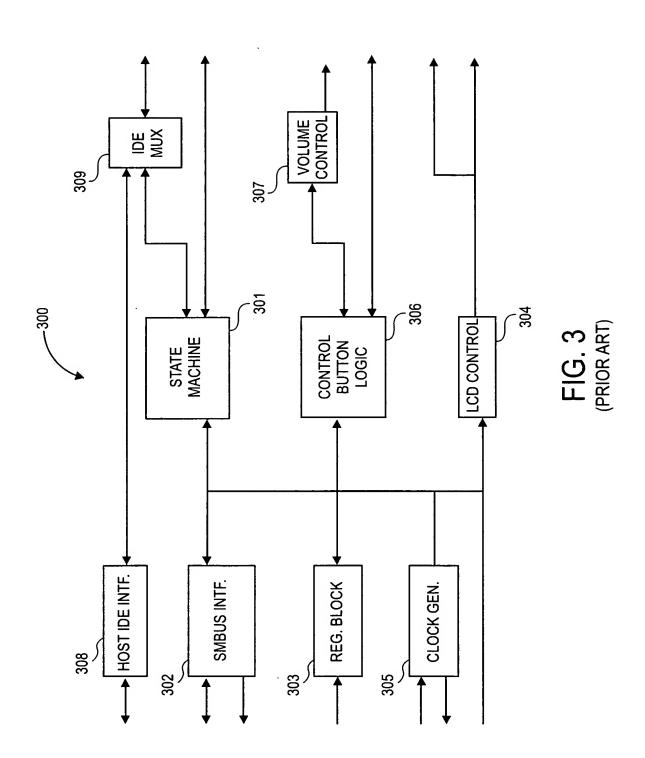


FIG. 1 (PRIOR ART)





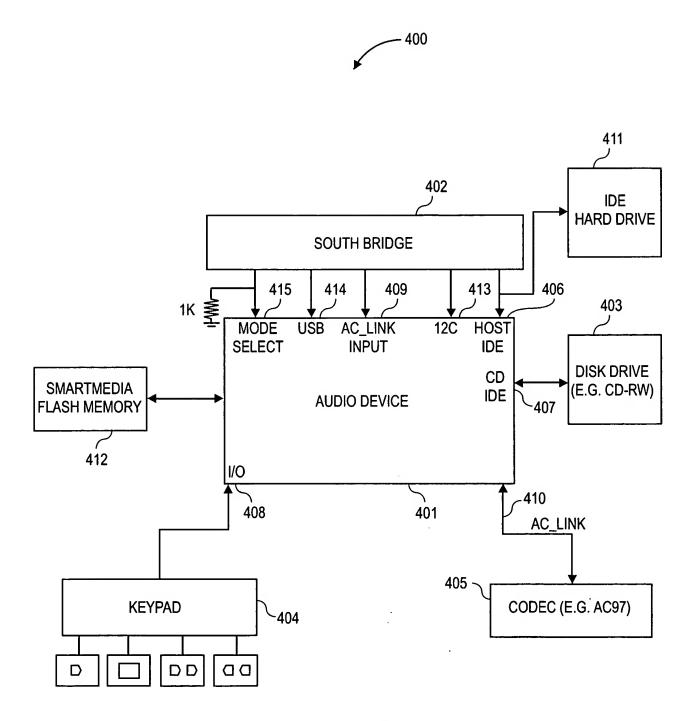


FIG. 4

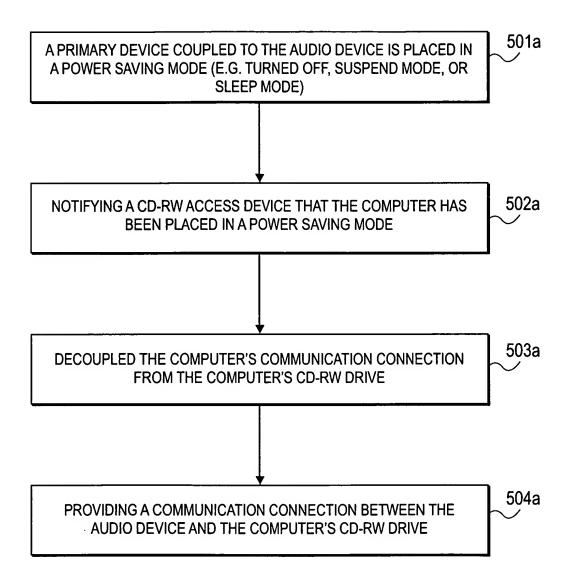


FIG. 5A

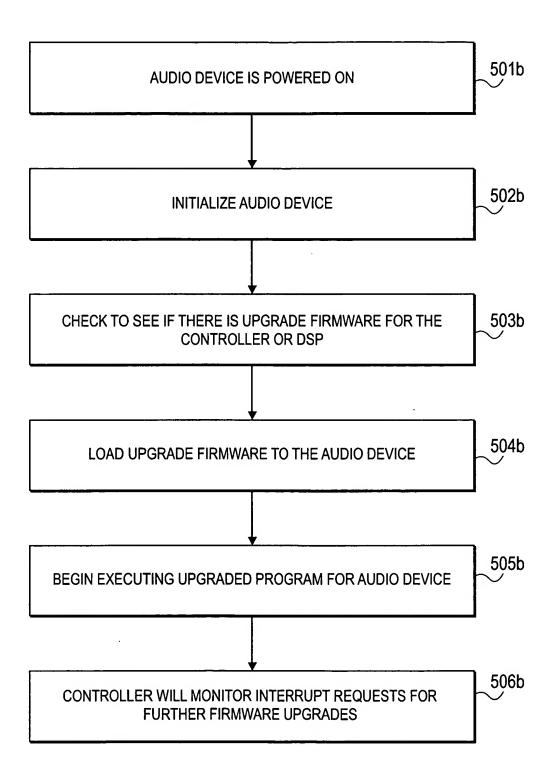


FIG. 5B

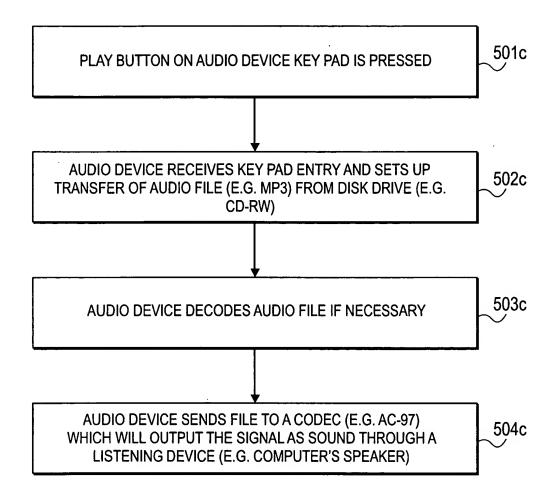


FIG. 5C

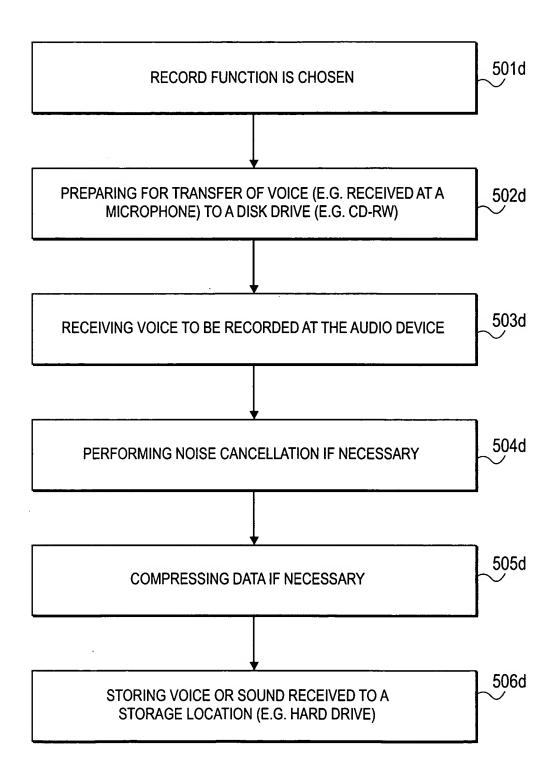


FIG. 5D

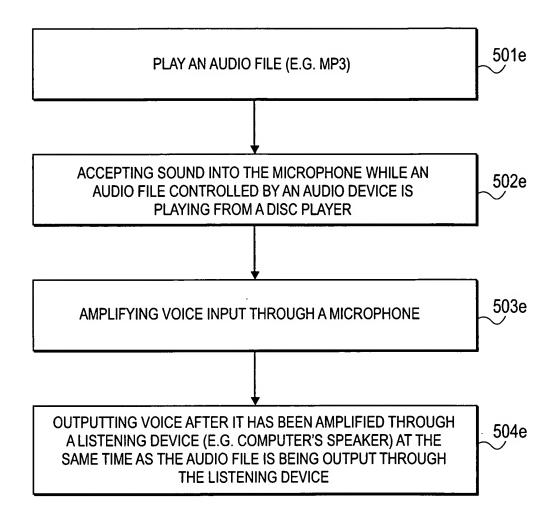


FIG. 5E

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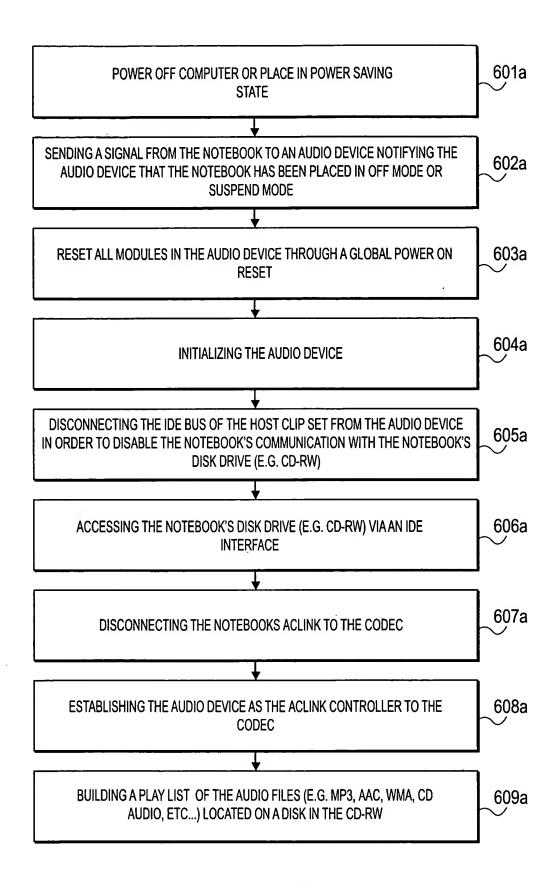
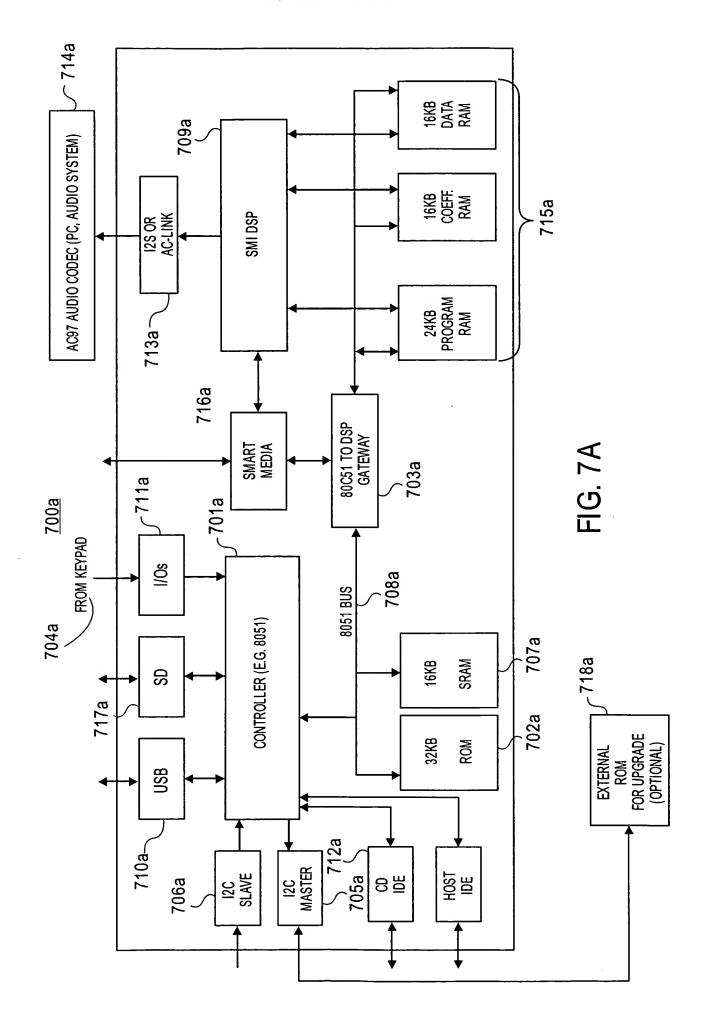


FIG. 6A

$\left\langle \begin{array}{c} 602 \\ 603 \end{array} \right\rangle \left\langle \begin{array}{c} 602 \\ 603 \end{array} \right\rangle$				
MODE	TRANSPARENT MODE	DSP MODE	SCAN MODE	TEST MODE
MODE_SELECT	1	0	0	1
TEST	0	0	ı	l

FIG. 6B



10/007,145 Replacement Sheet 13/20

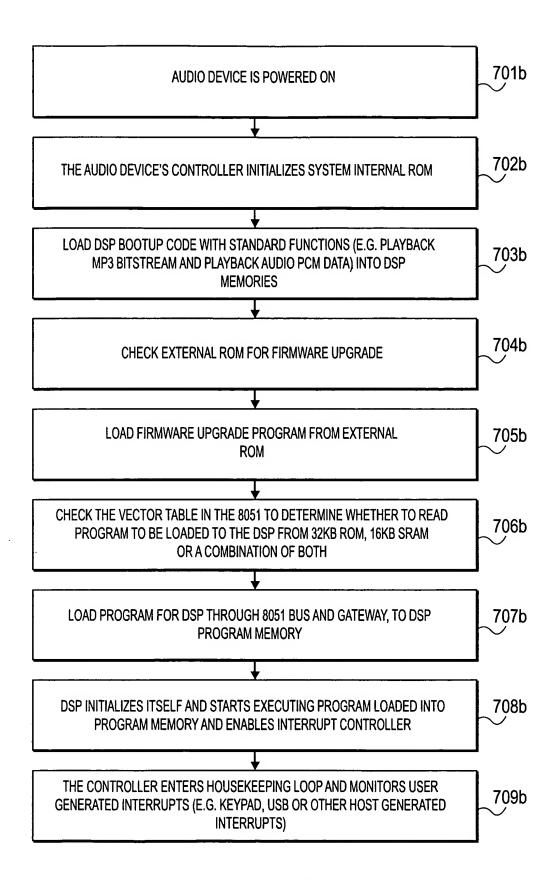


FIG. 7B

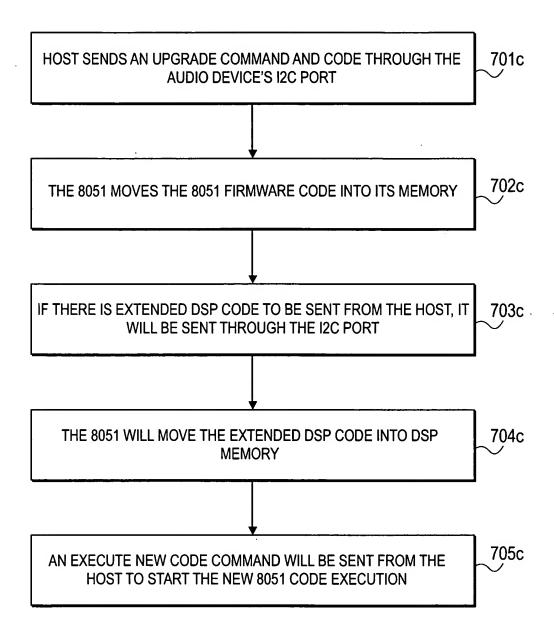


FIG. 7C

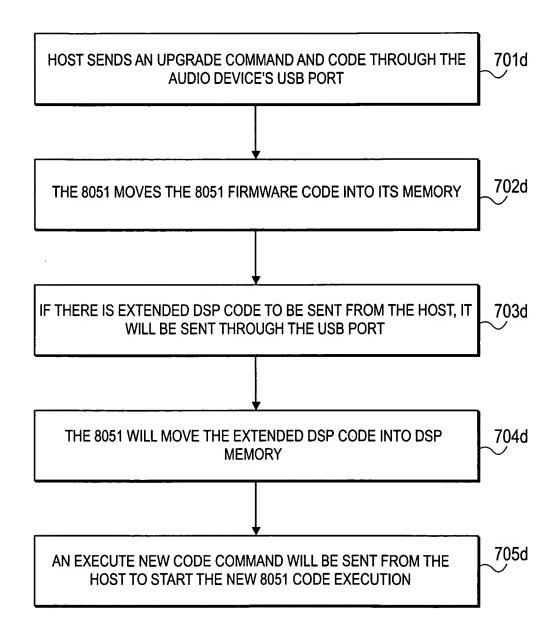


FIG. 7D

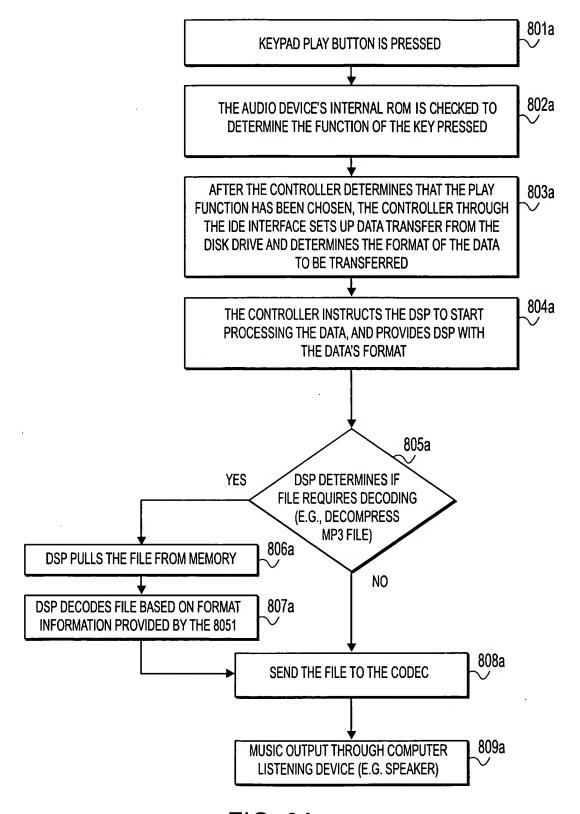


FIG. 8A

FIG. 8B

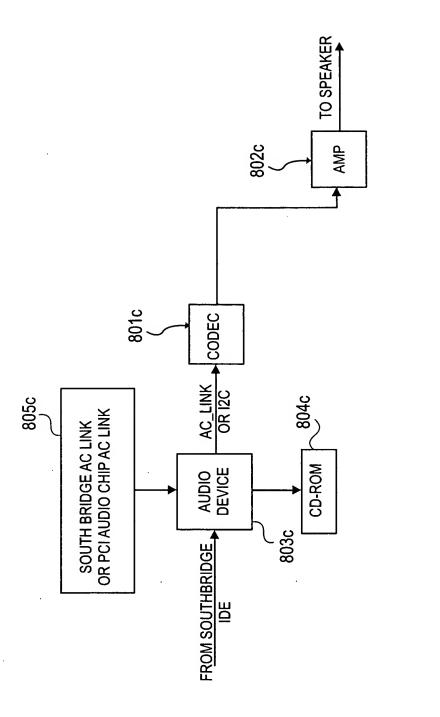


FIG. 80

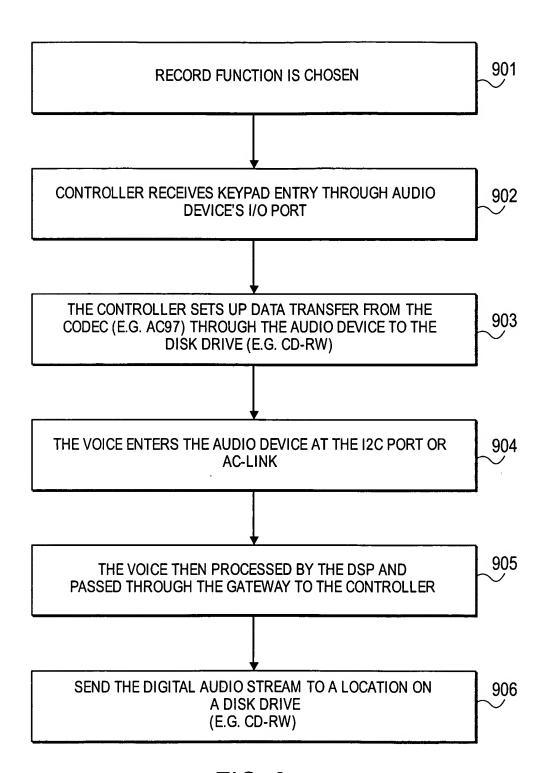


FIG. 9



